

M-bus Command	Contents	M-bus register header DIF	M-bus register VIF	Response/example	Remarks	LCD page
1. REQ UD2: 10 5B xx				68 xx xx 68 08 xx 72	68 [data length] 68 08 [address] 72 [header] [datablocks] [checksum] 16	
2. EEPROM contents: 68 03 03 68 53 xx B4						
3. RAM Contents: 68 03 03 68 53 xx B1						
	Serial number			46 02 02 16		
	Manufacturer ID			25 CD		
	Version			01	Same as the mayor version of the software	
	Medium			02	Electricity	
	Access number			04	Every time the meter is read this number is increased by 1 up to 255, then it become 0 again	
	Status			00	00 = OK 02 = error	
	Signature			00 00	Always 00 00	

1. REQ UD2						
10 5B 00						
		Datablocks:		68 xx xx [Data length] 68 08 xx [Address] 72		
Total active energy	0C	04	95 42 00 00		42,95kWh	
Total active energy T1	8C 10	04	83 42 00 00		42,83	
Total active energy T2	8C 10	04	12 00 00 00		0,12	
Total forward active energy	1C	04	04 46 00 00		46,04	
Forward active energy T1	9C 10	04	27 44 00 00		44,27	
Forward active energy T2	9C 20	04	77 01 00 00		1,77	
Total reverse active energy	2C	04	09 03 00 00		3,09	
Reverse active energy T1	AC 10	04	44 01 00 00		1,44	
Reverse active energy T2	AC 20	04	65 01 00 00		1,65	
Checksum			15 16		xx 16	

2. EEPROM contents						
68 03 03 68 53 00 B4						
		Datablocks:		68 E9 E9 68 08 00 72		
Hardware version	0A	FD 0C	04 01		1,04	
Firmware version	0A	FD 0E	18 02		2,18	
Meter max ampere	0A	FD 5C	00 01		100	
S0 output rate	0C	FD 3A	00 00 00 01		10000	
Combined code	09	FD 3A	10		10	
Total reactive energy	0E	7C 04 68 72 61 76	20 02 00 00 00 00		0,22	
Total reactive energy T1	8E 10	7C 04 68 72 61 76	80 02 00 00 00 00		0,28	
Total reactive energy T2	8E 20	7C 04 68 72 61 76	60 00 00 00 00 00		0,06	
Forward reactive energy	1E	7C 04 68 72 61 76	40 03 00 00 00 00		0,34	
Forward reactive energy T1	9E 10	7C 04 68 72 61 76	70 01 00 00 00 00		0,17	
Forward reactive energy T2	9E 20	7C 04 68 72 61 76	70 01 00 00 00 00		0,17	
Reverse reactive energy	2E	7C 04 68 72 61 76	60 05 00 00 00 00		0,56	
Reverse reactive energy T1	AE 10	7C 04 68 72 61 76	50 04 00 00 00 00		0,45	
Reverse reactive energy T2	AE 20	7C 04 68 72 61 76	10 01 00 00 00 00		0,11	
Blocked (only for CT version)	49	FD 3A	02		02	
CT rate (only for CT version)	0A	FD 3A	00 00			
L1 total active energy	4C	04	14 15 00 00		15,14	
L2 total active energy	8C 01	04	58 13 00 00		13,58	
L3 total active energy	CC 01	04	23 14 00 00		14,23	
L1 forward active energy	5C	04	29 16 00 00		16,29	
L2 forward active energy	9C 01	04	55 14 00 00		14,55	
L3 forward active energy	DC 01	04	20 15 00 00		15,20	
L1 reverse active energy	6C	04	15 01 00 00		1,15	
L2 reverse active energy	AC 01	04	97 00 00 00		0,97	
L3 reverse active energy	EC 01	04	97 00 00 00		0,97	
Checksum			C9 16		xx 16	
Header			68 90 90 68 08 00 72		68 xx xx [Data length] 68 08 xx [Address] 72	
L1 total reactive energy	4E	7C 04 68 72 61 76	70 00 00 00 00 00		0,07	
L2 total reactive energy	8E 01	7C 04 68 72 61 76	50 01 00 00 00 00		0,15	
L3 total reactive energy	CE 01	7C 04 68 72 61 76	00 00 00 00 00 00		0	
L1 forward reactive energy	5E	7C 04 68 72 61 76	30 01 00 00 00 00		0,13	
L2 forward reactive energy	9E 01	7C 04 68 72 61 76	90 00 00 00 00 00		0,09	
L3 forward reactive energy	DE 01	7C 04 68 72 61 76	20 01 00 00 00 00		0,12	
L1 reverse reactive energy	6E	7C 04 68 72 61 76	00 02 00 00 00 00		0,20	
L2 reverse reactive energy	AE 01	7C 04 68 72 61 76	40 02 00 00 00 00		0,24	
L3 reverse reactive energy	EE 01	7C 04 68 72 61 76	20 01 00 00 00 00		0,12	
Resettable kWh	0C	04	18 00 00 00		0,18	
Checksum			69 16		xx 16	

3. RAM contents						
68 03 03 68 53 00 B1						
		Datablocks:		68 E4 E4 68 08 00 72		
L1 voltage	4B	FD 47	30 27 02		227,3V	
L2 voltage	8B 01	FD 47	00 00 00		0	
L3 voltage	CB 01	FD 47	00 00 00		0	
L1 current	4B	FD 5A	16 04 00		4,16	
L2 current	8B 01	FD 5A	00 00 00		0	
L3 current	CB 01	FD 5A	00 00 00		0	
Total active power	0C	2B	46 09 00 00		0,946	
L1 active power	4C	2B	46 09 00 00		0,946	
L2 active power	8C 01	2B	00 00 00 00		0	
L3 active power	CC 01	2B	00 00 00 00		0	
Total reactive power	0C	7C 03 72 61 76	00 00 00 00		0	
L1 reactive power	4C	7C 03 72 61 76	00 00 00 00		0	
L2 reactive power	8C 01	7C 03 72 61 76	00 00 00 00		0	
L3 reactive power	CC 01	7C 03 72 61 76	00 00 00 00		0	
Total apparent power	0C	7C 02 41 56	46 09 00 00		0,946	
L1 apparent power	4C	7C 02 41 56	46 09 00 00		0,946	
L2 apparent power	8C 01	7C 02 41 56	00 00 00 00		0	
L3 apparent power	CC 01	7C 02 41 56	00 00 00 00		0	
Total power factor	0A	FD 3A	00 01		1	
L1 power factor	4A	FD 3A	00 01		1	
L2 power factor	8A 01	FD 3A	00 00		0	
L3 power factor	CA 01	FD 3A	00 00		0	
Grid frequency	0A	7C 02 7A 48	03 50		50,30	
Tariff	09	7C 01 54	02		2	
Comb. Active status word	0B	FD 17	00 11 01		11 100	
Power on off counter	0A	FD 60	12 00		12	
4 Q reading	09	7C 01 51	04		4	
L1 4Q	49	7C 01 51	04		4	
L2 4Q	89 01	7C 01 51	03		3	
L3 4Q	C9 01	7C 01 51	03		3	
Checksum			81 16		xx 16	

Default	
Baudrate	2400
Databits	8
Parity	Even
Stopbit	1
Address	00
Broadcast primary address	FE

CRC settings without checksum	
Start byte REQ UD2	2
Start byte EEPROM contents	5
Start byte RAM contents	5
CRC type	SUM
Terminating symbol	16
HEX	-
Low byte first	-
1 byte	-

CRC settings with checksum	
No CRC	-