M-bus Command	Contents	M-bus register header DIF	M-bus register VIF	Response/example	Remarks	LCD page
1. REQ UD2: 10 5B xx				68 xx xx 68 08 xx 72	68 **[data length]** 68 08 **[address]** 72 **[header]** **[datablocks]** **[checksum]** 16	/
2. EEPROM contents: 68 03 03 68 53xx B4						
3. RAM Contents: 68 03 03 68 53xx B1						
	Serial number			99 99 92 15	Serial number 15029999	/
	Manufacturer ID			92 B0		/
	Version			01	Same as the mayor version of the software	/
	Medium	**Header**		02	Electricity	/
	Acces number			0A	Every time the meter is read this number is increased by 1 up to 255, then it becomse 0 again	/
	Status			00	00 = OK 02 = error	/
	Signature			00 00	Always 00 00	/

1. REQ UD2						
10 5B 00 5B 16				68 4B 4B 68 08 00 72	68 xx xx [Data length] 68 08 xx [Address] 72	
		Datablocks:				
	Total active energy	0C	04	03 00 00 00	0,03 kWh	02
	Total active energy T1	8C10	04	03 00 00 00	0,03 kWh	
	Total active energy T2	8C20	04	00 00 00 00	0 kWh	
	Total forward active energy	1C	04	03 00 00 00	0,03 kWh	04
	Forward active energy T1	9C10	04	03 00 00 00	0,03 kWh	08
	Forward active energy T2	9C20	04	00 00 00 00	0 kWh	12
	Total reverse active energy	2C	04	00 00 00 00	0 kWh	05
	Reverse active energy T1	AC10	04	00 00 00 00	0 kWh	09
	Reverse active energy T2	AC20	04	00 00 00 00	0 kWh	13
	Checksum			B2 16	xx 16	

2. EEPROM contents						
68 03 03 68 53 00 B4				68 99 99 68 08 00 72	68 xx xx [Data length] 68 08 xx [Address] 72	
		Datablocks:				
	Hardware version	0A	FD 0C	01 01	Version 1.03	
	Firmware version	0A	FD 0E	14 01	Version 1.14	
	Meter max ampere	0B	FD 59	00 45 00	45A	
	S0 output rate	0B	FD 3A	00 00 10 00	001000,00 imp/kWh	25
	Combined code	09	FD 3A	10	10 combined code = ..+.. / 01= .. 02=..	26
	Total reactive energy	0C	7C 04 76 61 72 68	06 00 00 00	0,06 kvarh	03
	Total reactive energy T1	8C 10	7C 04 76 61 72 68	06 00 00 00	0,06 kvarh	
	Total reactive energy T2	8C 20	7C 04 76 61 72 68	06 00 00 00	0 kvarh	
	Forward reactive energy	1C	7C 04 76 61 72 68	06 00 00 00	0,06 kvarh	06
	Forward reactive energy T1	9C 10	7C 04 76 61 72 68	06 00 00 00	0,06 kvarh	10
	Forward reactive energy T2	9C20	7C 04 76 61 72 68	00 00 00 00	0 kvarh	14
	Reverse reactive energy	2C	7C 04 76 61 72 68	00 00 00 00	0 kvarh	07
	Reverse reactive energy T1	AC 10	7C 04 76 61 72 68	00 00 00 00	0 kvarh	11
	Reverse reactive energy T2	AC 20	7C 04 76 61 72 68	00 00 00 00	0 kvarh	15
	Resettable kWh	0C	04	13 00 01 00	100,13 kWh	29
	Checksum			DF 16	xx 16	

3. RAM contents						
68 03 03 68 53 00 B1				68 45 45 68 08 00 72	68 xx xx [Data length] 68 08 xx [Address] 72	
		Datablocks				
	Voltage	0B	FD 47	50 32 02	232.5V	16
	Current	0B	FD 59	65 06 00	6,65A	17
	Active power	0C	2A	11 10 00 00	10,11kW	19
	Reactive power	0C	7C 03 76 61 72	02 00 00 00	0,02kvar	20
	Apparent power	0C	7C 02 56 41	11 10 00 00	10,11kVA	21
	Power factor	0A	FD 3A	00 01	1.00	22
	Frequency	0A	7C 02 48 7A	00 50	50Hz	18
	Tariff	09	7C 01 54	01	1	/
	Checksum			2F 16		

Default	
Baudrate	9600
Databits	8
Parity	Even
Stopbit	1
Address	00
Broadcast primary address	FE (only for read)

CRC settings without checksum	
Start byte REQ UD2	2
Start byte EEPROM contents	5
Start byte RAM contents	5
CRC type	SUM
Terminating symbol	16
HEX	-
Low byte first	-
1 byte	-

CRC settings with checksum	
No CRC	-